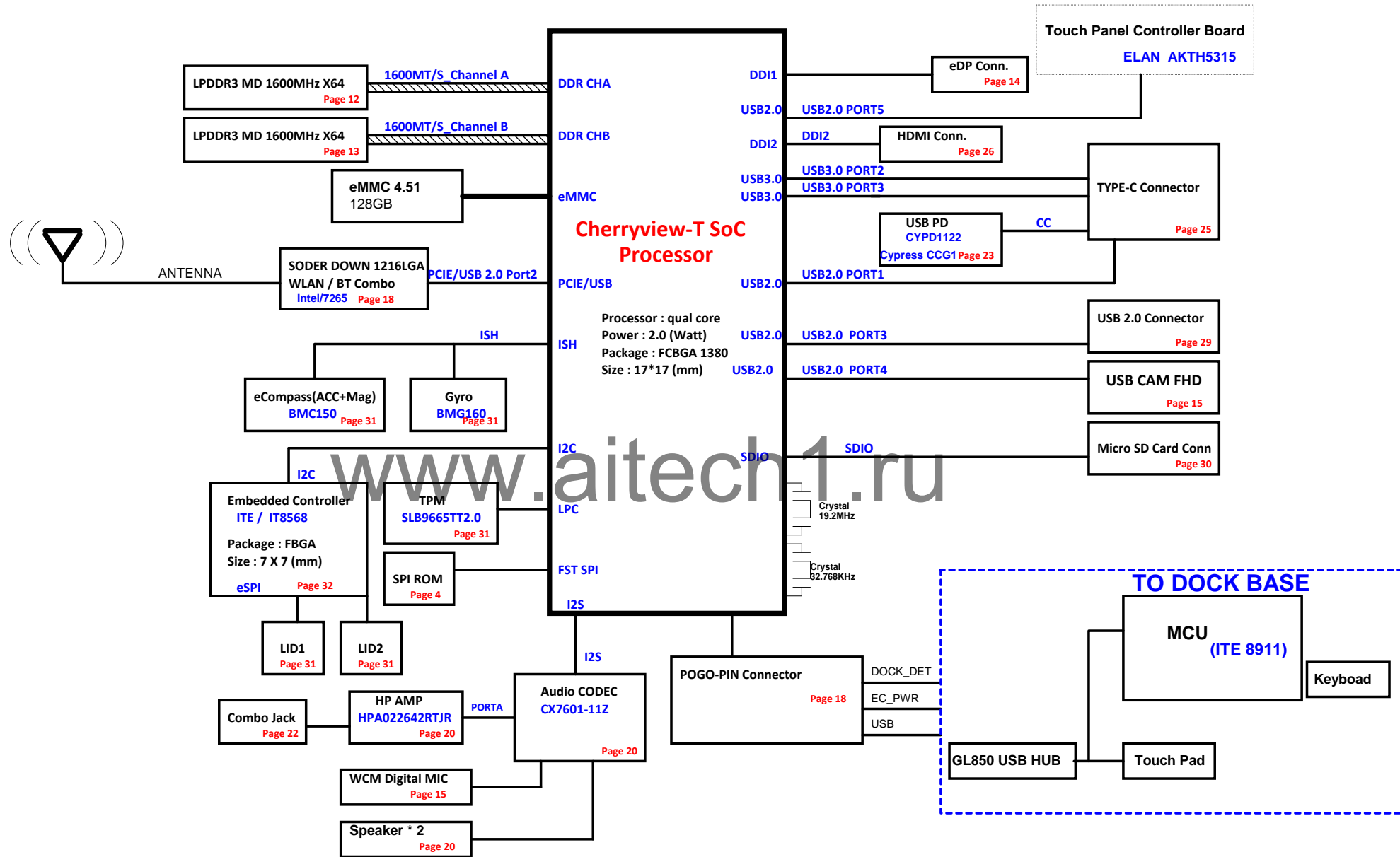



Gummi Cherry Trail Block Diagram

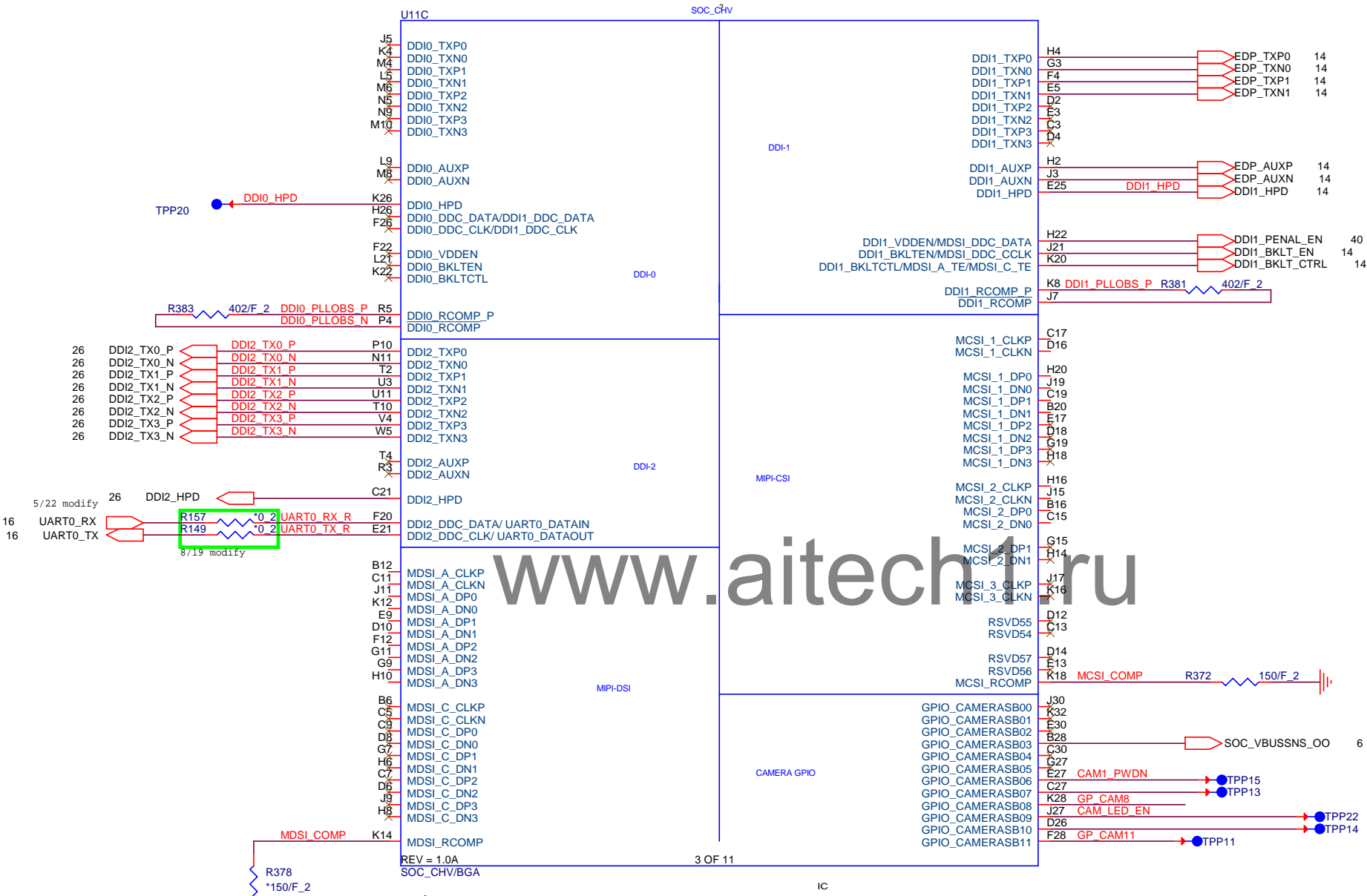
01



	PROJECT : Gummi		
	Quanta Computer Inc.		
	Size	Document Number	Rev
	NB5 HW	Block diagram	1A
Date: Monday, October 26, 2015			
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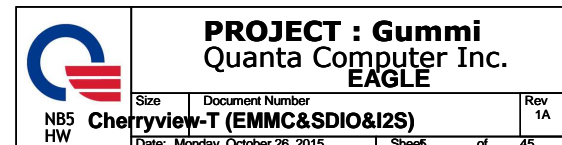
Hardware Strap

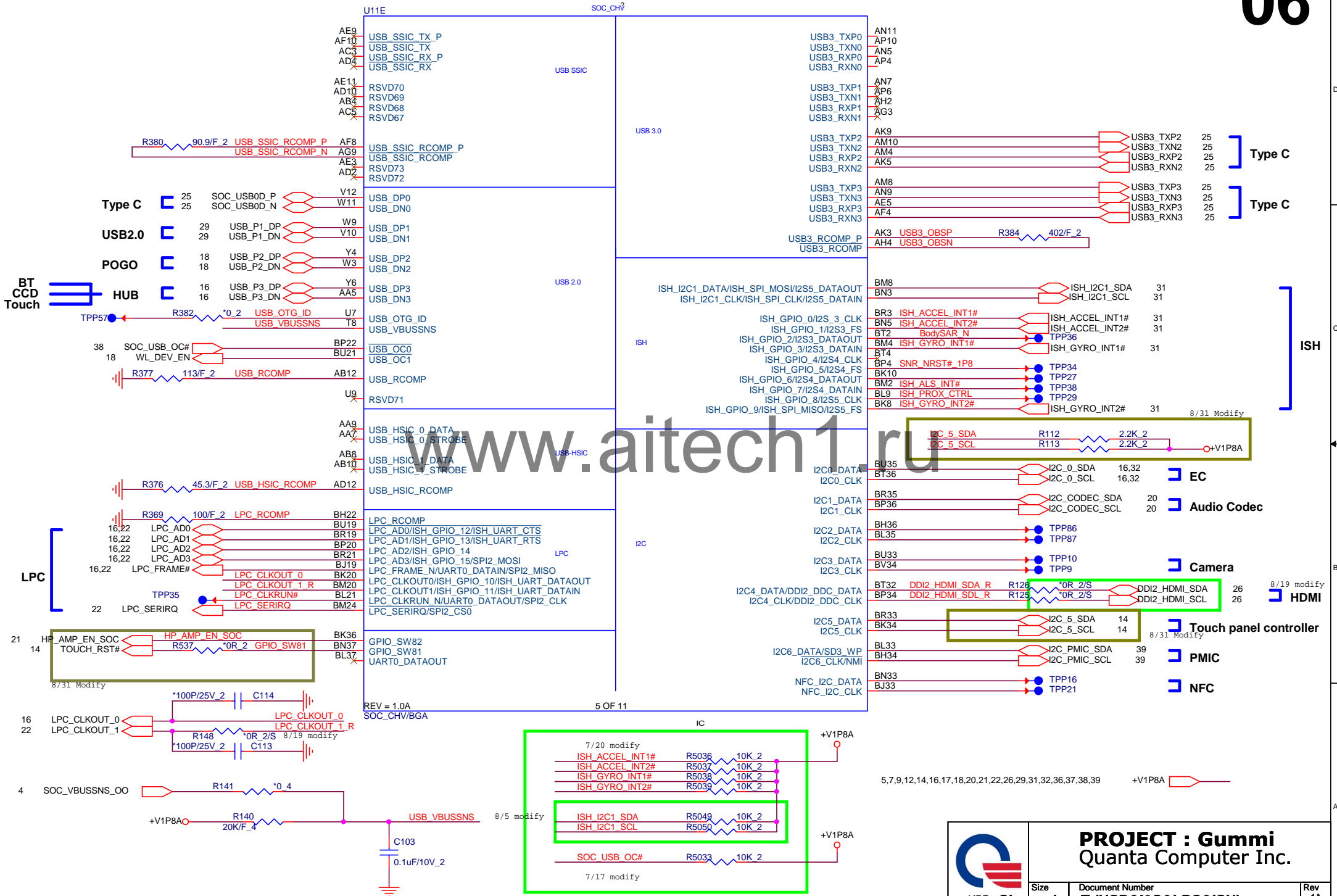
Pin Name	Purpose	Polarity	Internal PU/PD	
GPIO_CAMERAB08	ICLK Xtal OSC Bypass	0 = No Bypass 1 = Bypass	PD	R144 *10K_2 GP_CAM8
GPIO_CAMERAB09	CCU SUS RO Bypass	0 = No Bypass 1 = Bypass	PD	R147 *10K_2 CAM_LED_EN
GPIO_CAMERAB11	RTC OSC Bypass	0 = No Bypass 1 = Bypass	PD	R128 *10K_2 GP_CAM11



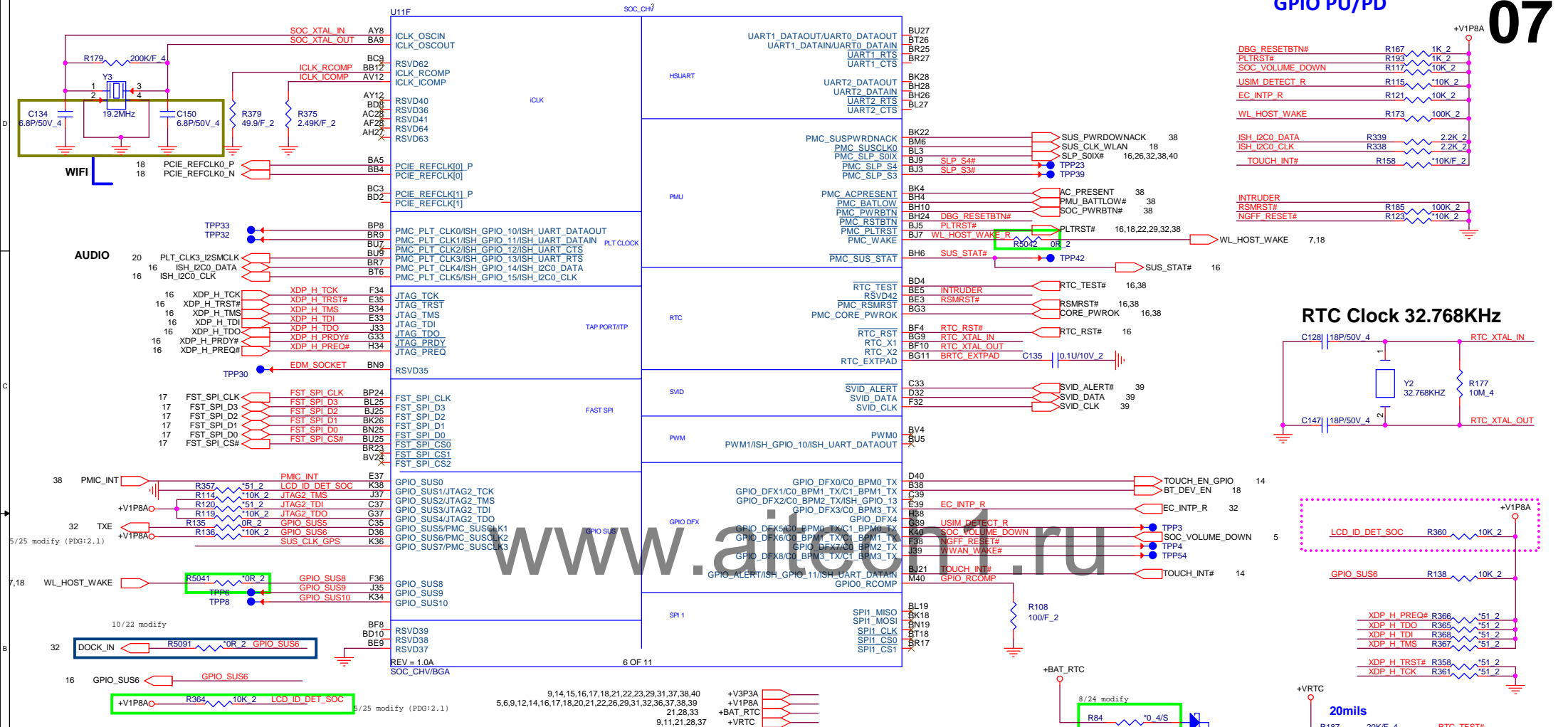
PROJECT : Gummi
Quanta Computer Inc.

Size	Document Number	Rev
	Cherryview-T (DISPLAY/CAM)	1A
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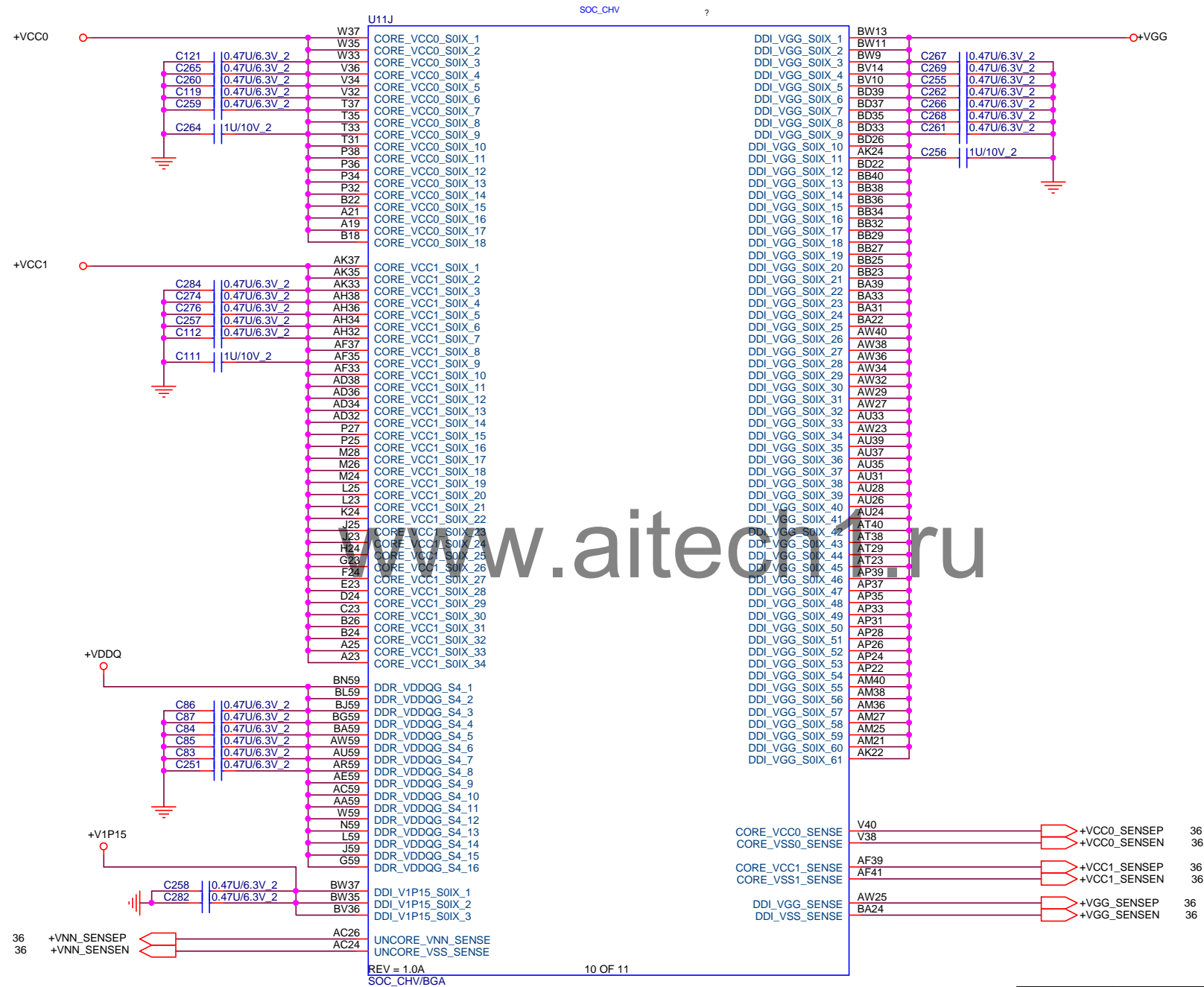


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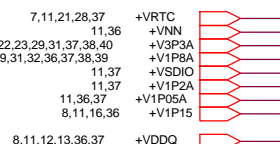
PROJECT : Gummi
Quanta Computer Inc.

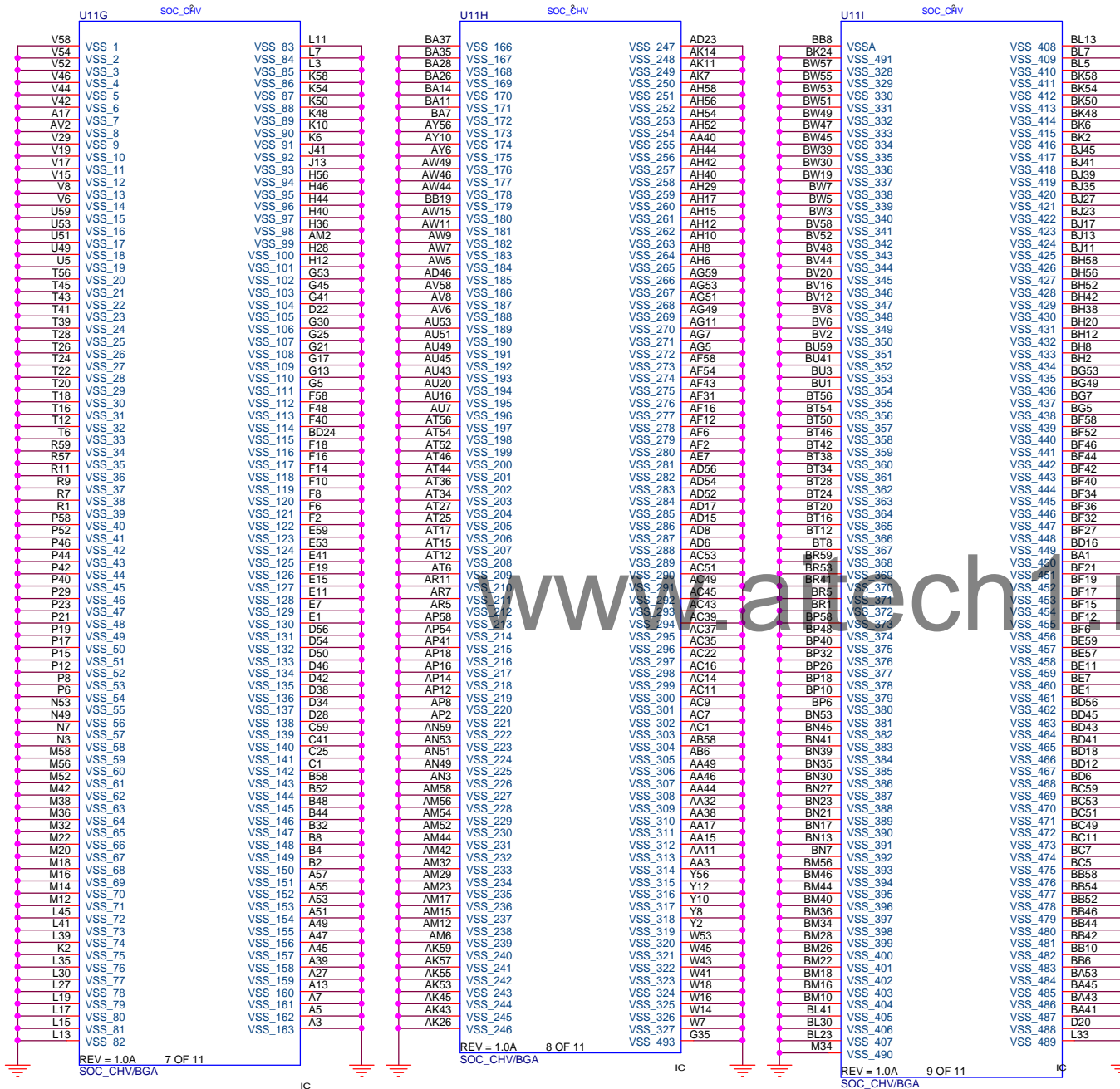
Size	Document Number	Rev
	Cherryview-T (CLK/UART/SP1/PMU)	1A
Date:	Monday, October 26, 2015	Sheet of 45



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Quanta Computer Inc.
EAGLE

Size	Document Number	Rev 1A
	Cherryview-T (POWER 1/2)	
Date:	Monday, October 26, 2015	Sheet of 45



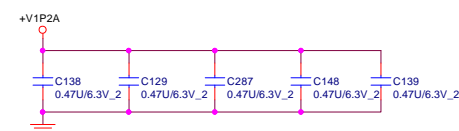


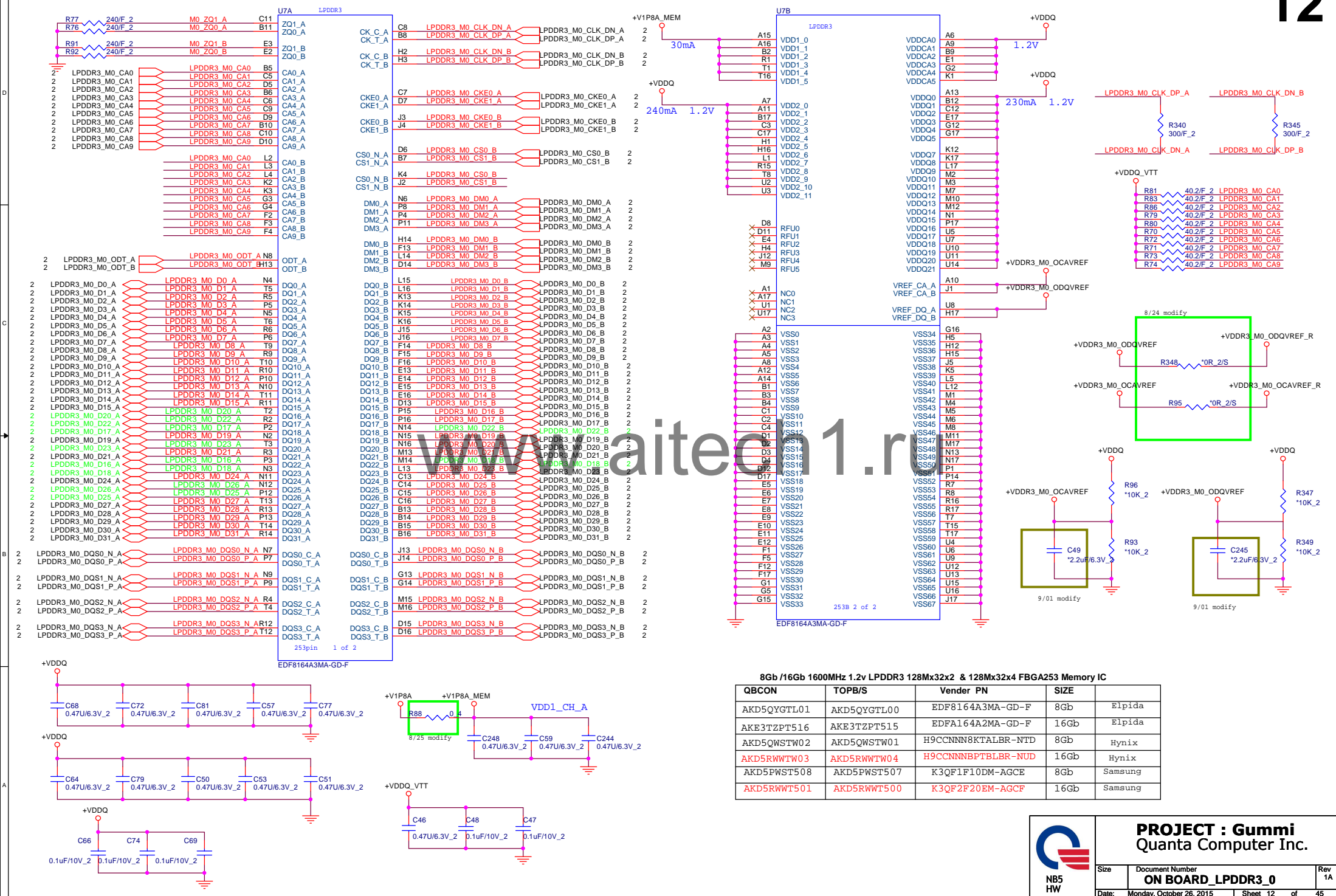
QBCON	TOPB/S	Vender PN
AJSR27MVT01	AJSR27MVT00	Z8700 1.6G SR27M
AJSR27NUT01	AJSR27NUT00	Z8500 2.24G SR27N




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Size	Document Number	Rev
	Valleyview-T (GND)	1A
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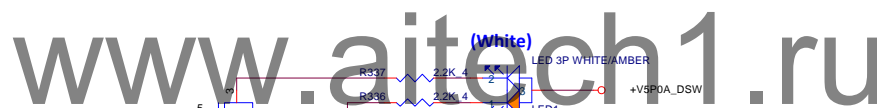


 NB5 HW	PROJECT : Gummi Quanta Computer Inc.		
	Size	Document Number ON BOARD_LPDDR3_1	Rev 1A
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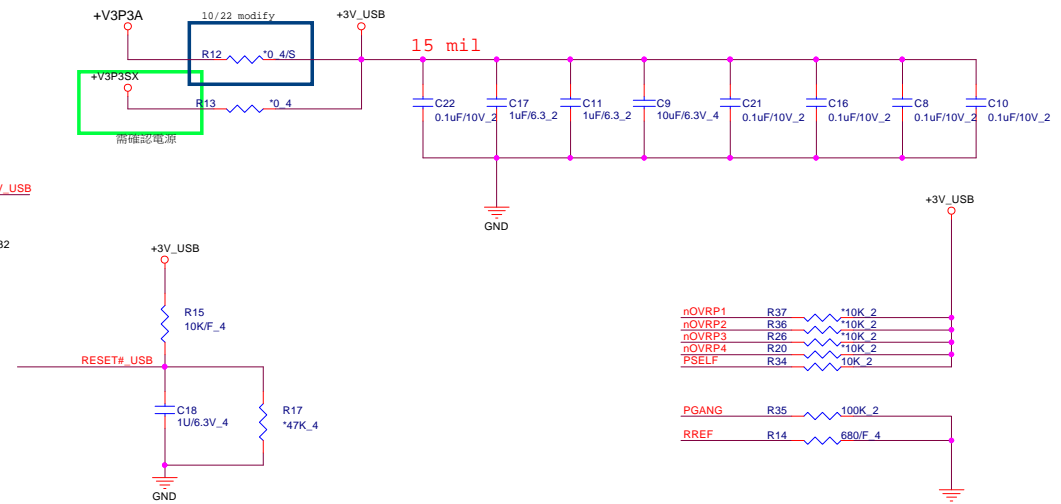
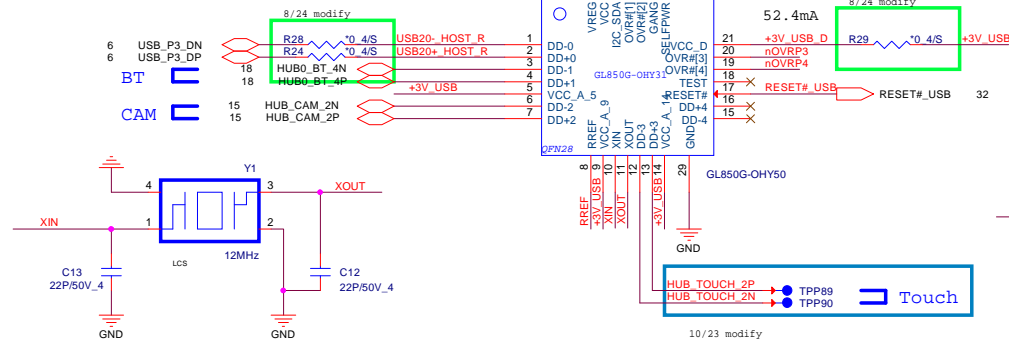
PROJECT : Gummi
Quanta Computer Inc.

Size	Document Number eDP	Rev 1A
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USB HUB

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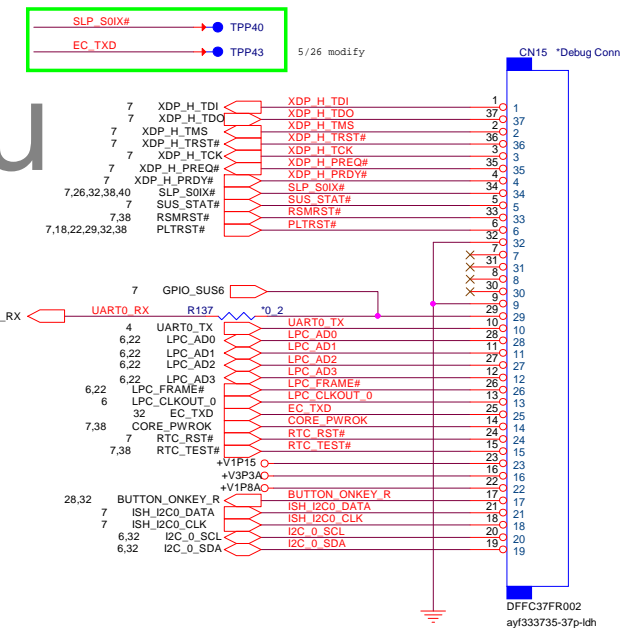


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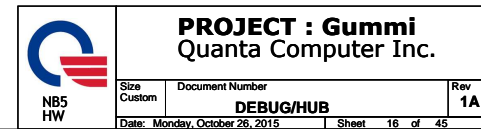
Intel XTP

80 Port Debug


SoC UART Debug



15,18,22,30,31,37	+V3P3SX
8,9,11,36	+V1P15
9,14,15,17,18,21,22,23,29,31,37,38,40	+V3P3A
5,6,7,9,12,14,17,18,20,21,22,26,29,31,32,36,37,38,39	+V1P8A






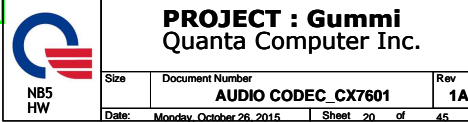
 NB5 HW	PROJECT : Gummi Quanta Computer Inc.		
	Size	Document Number eMMC/SPI ROM	Rev 1A
	Date: Monday, October 26, 2015	Sheet 17 of 45	

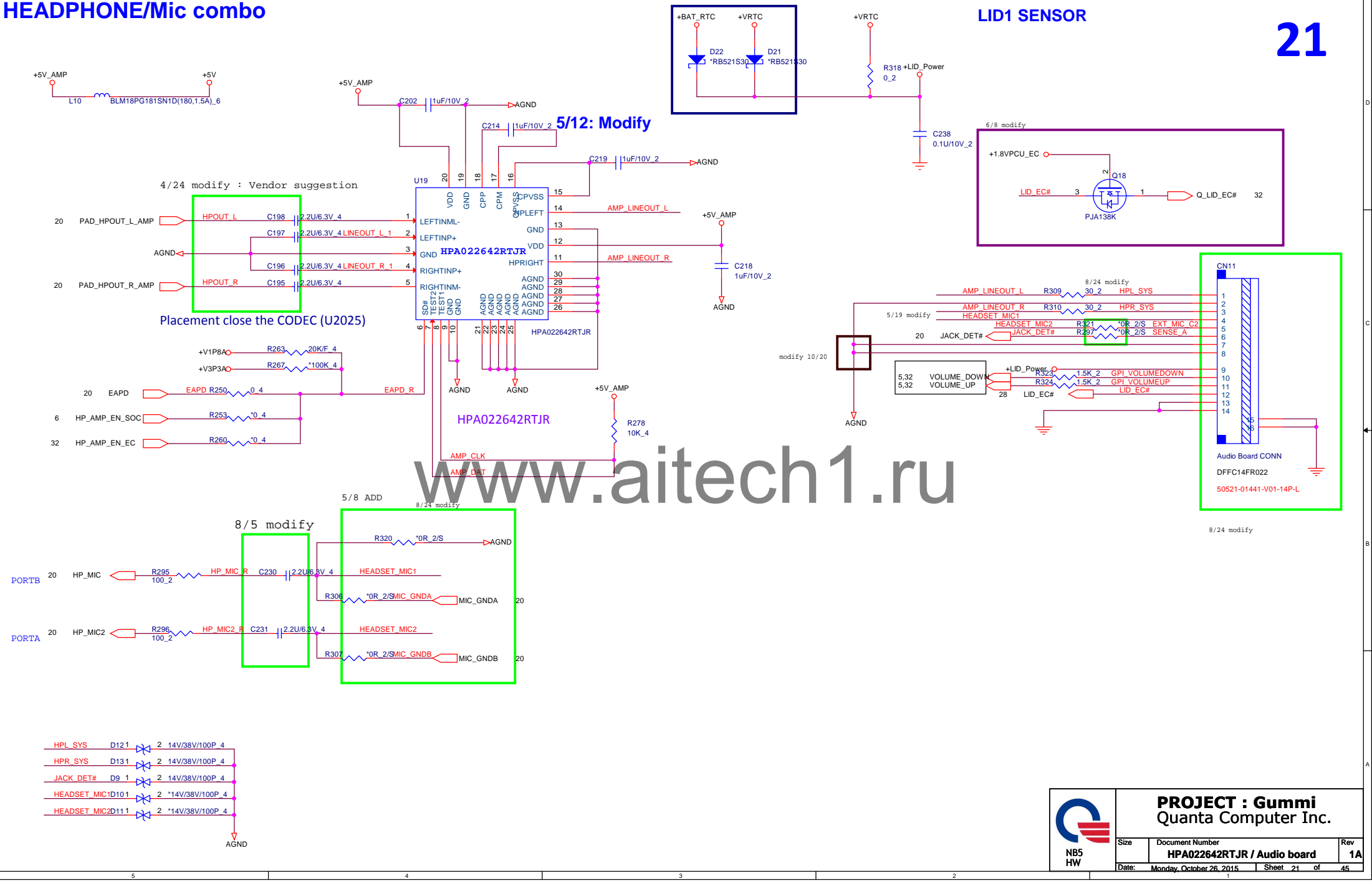


Size Custom	Document Number WIFI/BT& POGO	Rev 1A
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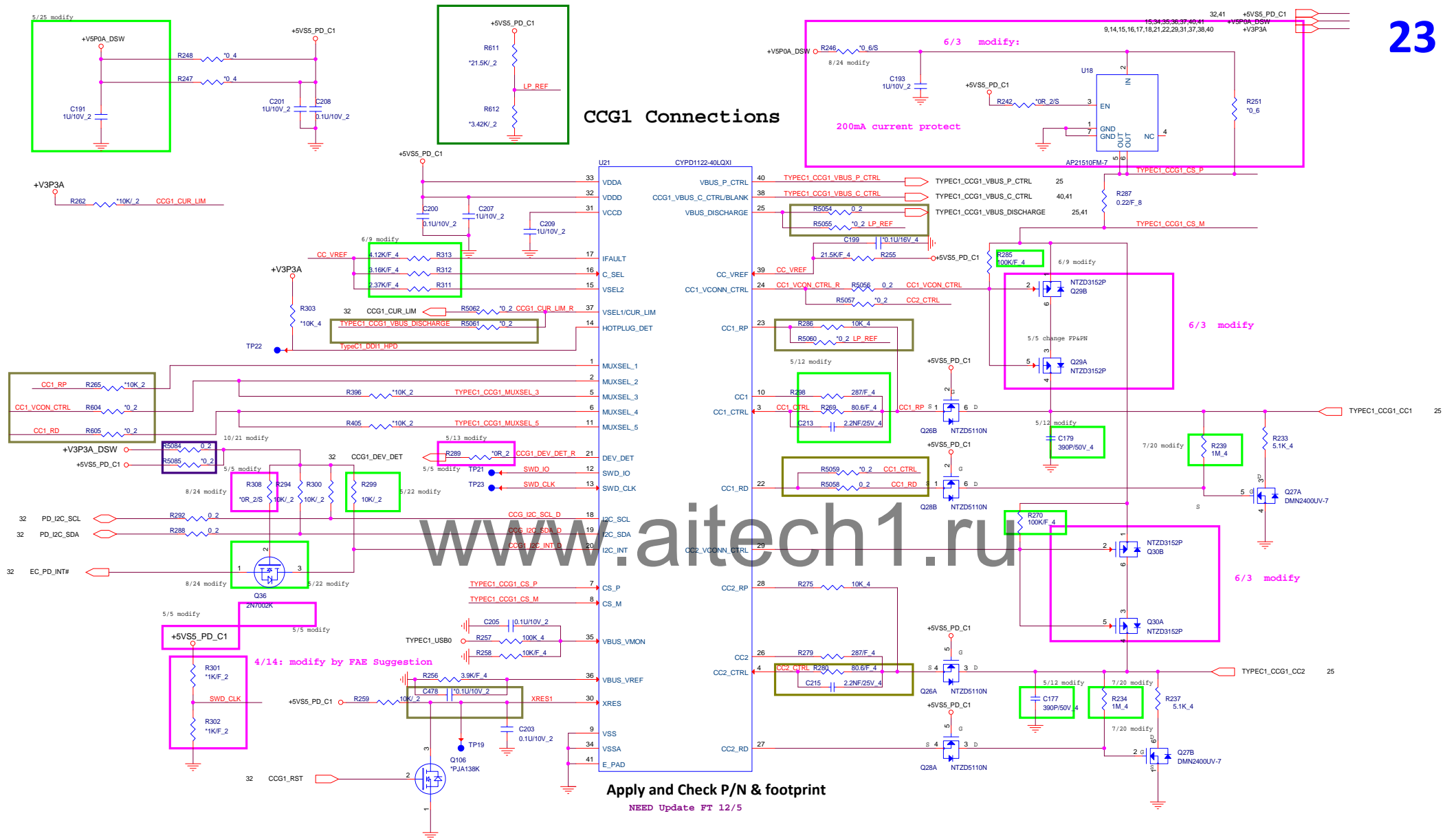
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 NB5 HW	PROJECT : Gummi Quanta Computer Inc.	Rev 1A
	Size Custom	
	Document Number WWAN Conn & SIM	
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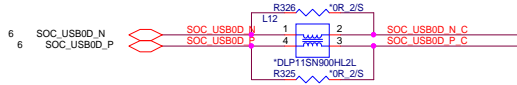
CCG1 Connections



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 NB5 HW	PROJECT : Gummi Quanta Computer Inc.		
	Size	Document Number	Rev
		PS8740B Pin Control _1	1A
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USB2.0 ESD



Type C1_HSIO_ESD

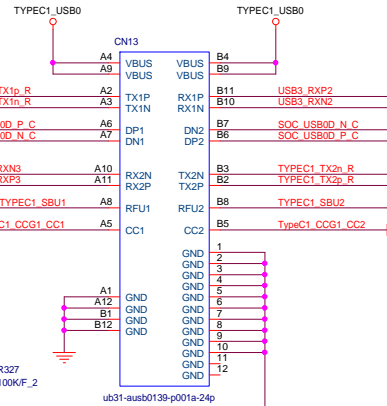
USB3.0 PORT2



USB3.0 PORT3



8/24 modify



Check P/N & footprint

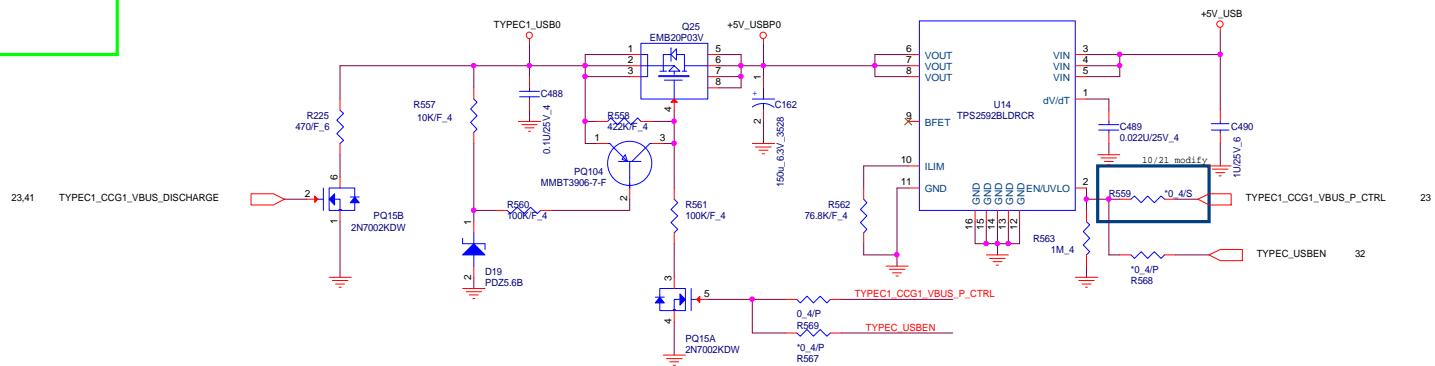
7/14 need close to U21

5/12: Modify

8/26 modify

8/26 modify

7/14 need close to U21

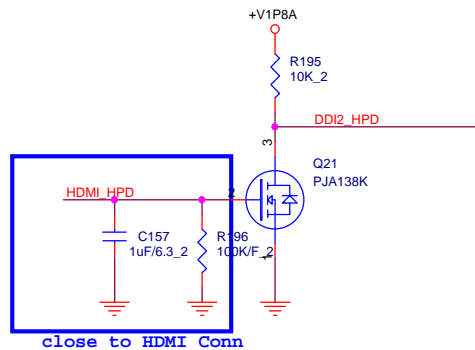


HDMI HOT PLUG

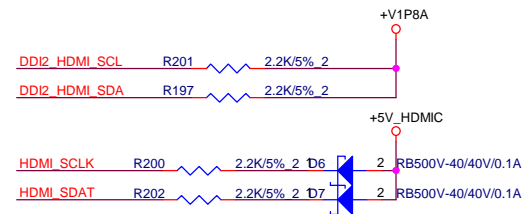
4	DDI2_TX2_N	DDI2_TX2_N	C315	0.1uF/10V_2C_TX0_HDMI-
4	DDI2_TX2_P	DDI2_TX2_P	C313	0.1uF/10V_2C_TX0_HDMI+
4	DDI2_TX1_N	DDI2_TX1_N	C312	0.1uF/10V_2C_TX1_HDMI-
4	DDI2_TX1_P	DDI2_TX1_P	C311	0.1uF/10V_2C_TX1_HDMI+
4	DDI2_TX0_N	DDI2_TX0_N	C310	0.1uF/10V_2C_TX2_HDMI-
4	DDI2_TX0_P	DDI2_TX0_P	C309	0.1uF/10V_2C_TX2_HDMI+
4	DDI2_TX3_N	DDI2_TX3_N	C317	0.1uF/10V_2C_TXC_HDMI-
4	DDI2_TX3_P	DDI2_TX3_P	C316	0.1uF/10V_2C_TXC_HDMI+

6	DDI2_HDMI_SCL	DDI2_HDMI_SCL
6	DDI2_HDMI_SDA	DDI2_HDMI_SDA
4	DDI2_HPD	DDI2_HPD

HDMI HOT PLUG



I2C Pull up

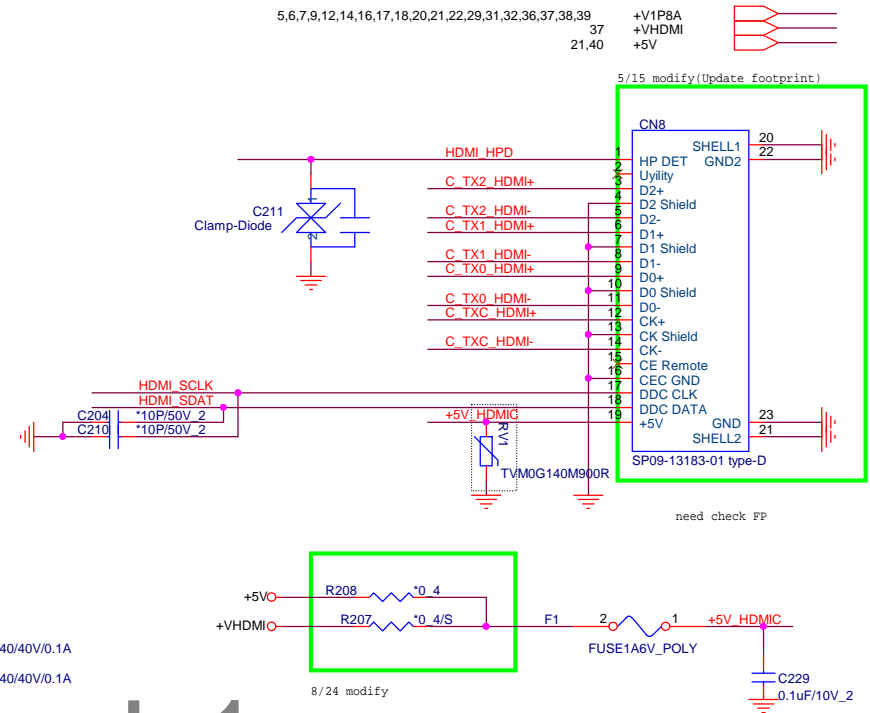


EMI Solution

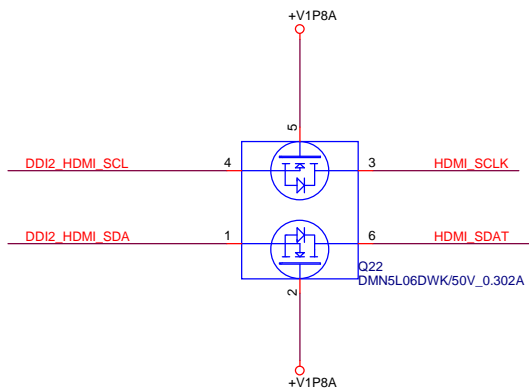
C_TX2_HDMI+	R395	180/F_2	C_TX2_HDMI-
C_TX1_HDMI+	R400	180/F_2	C_TX1_HDMI-
C_TX0_HDMI+	R403	180/F_2	C_TX0_HDMI-
C_TXC_HDMI+	R409	180/F_2	C_TXC_HDMI-

10/21 modify

26



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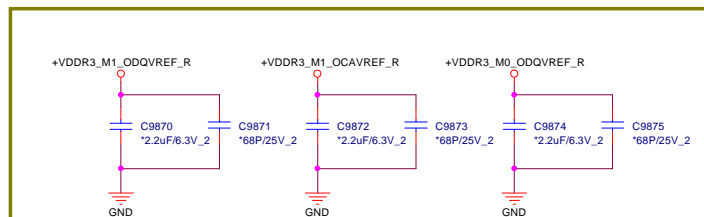
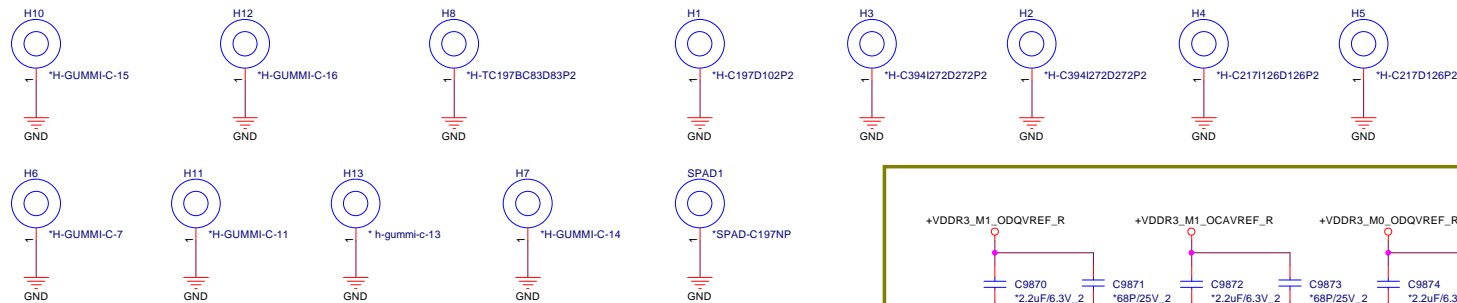


5/12 modify

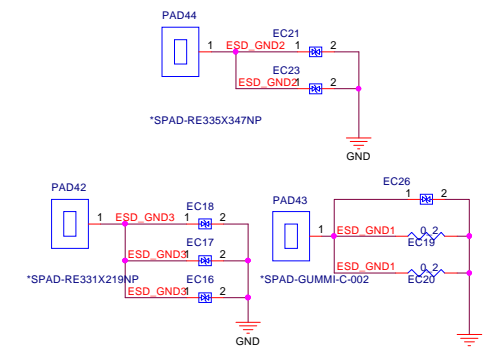
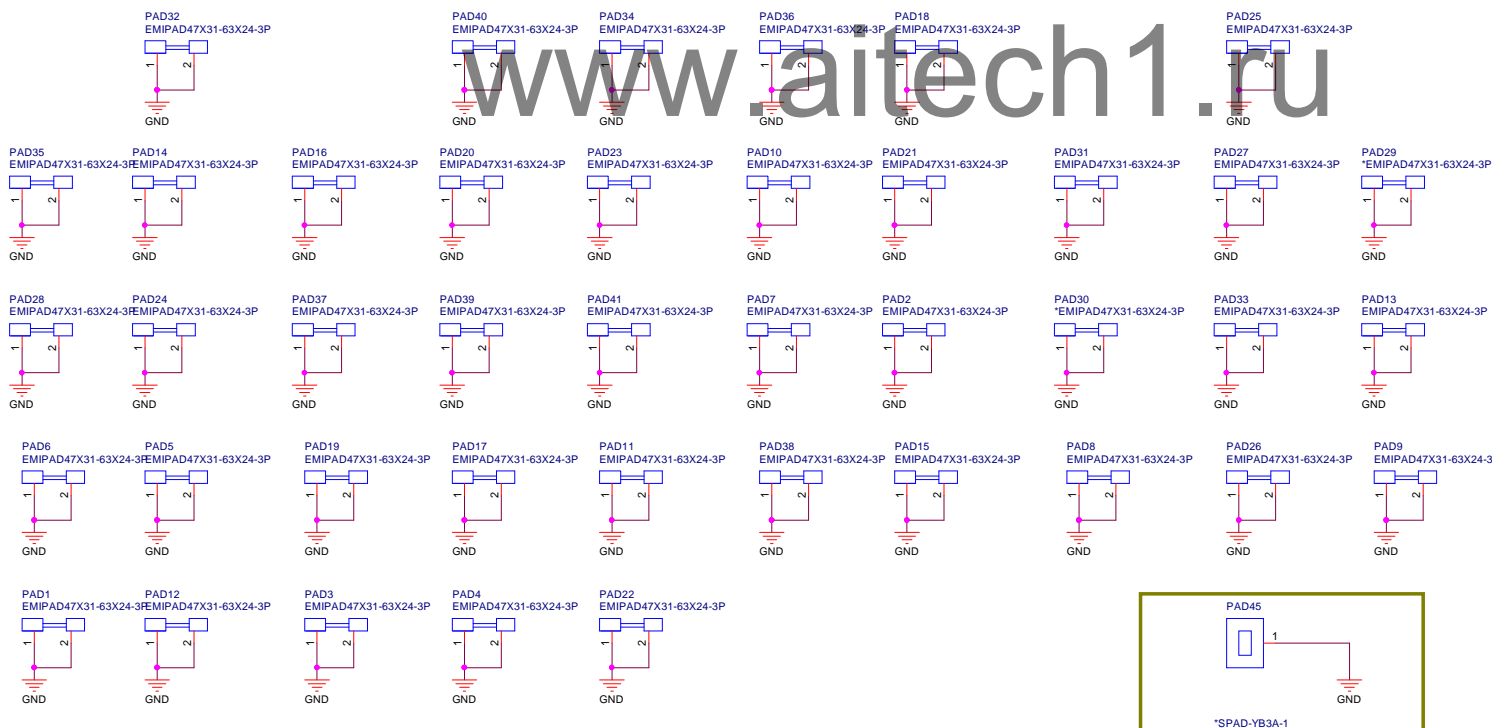
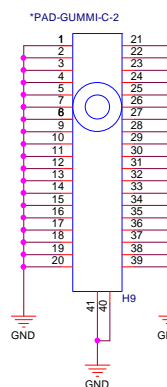
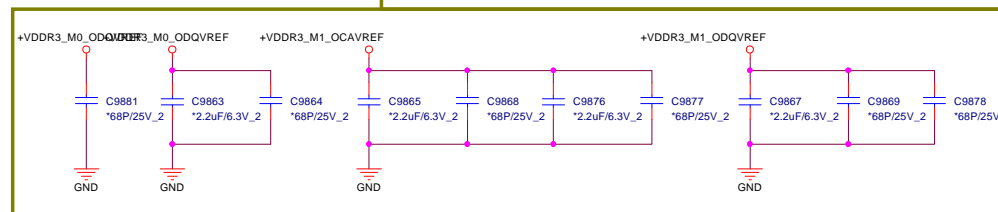


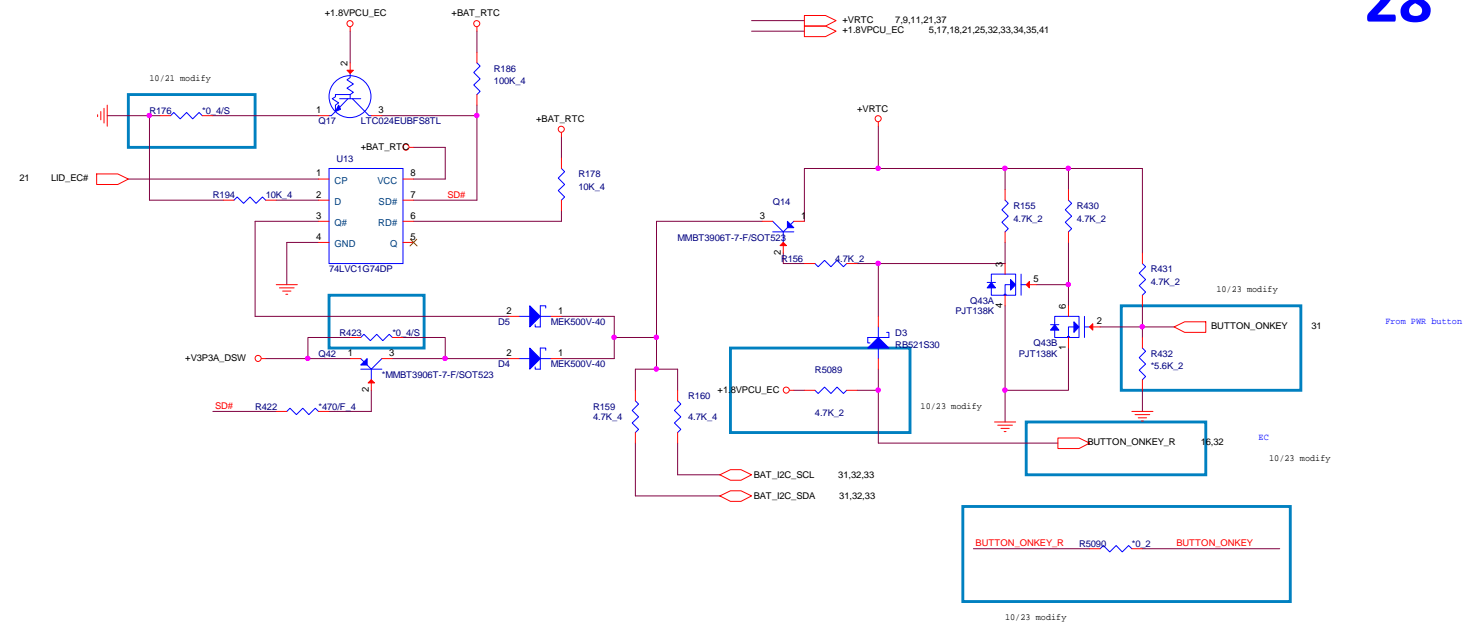
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Quanta Computer Inc.

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	Micro HDMI	1A
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9/01modify





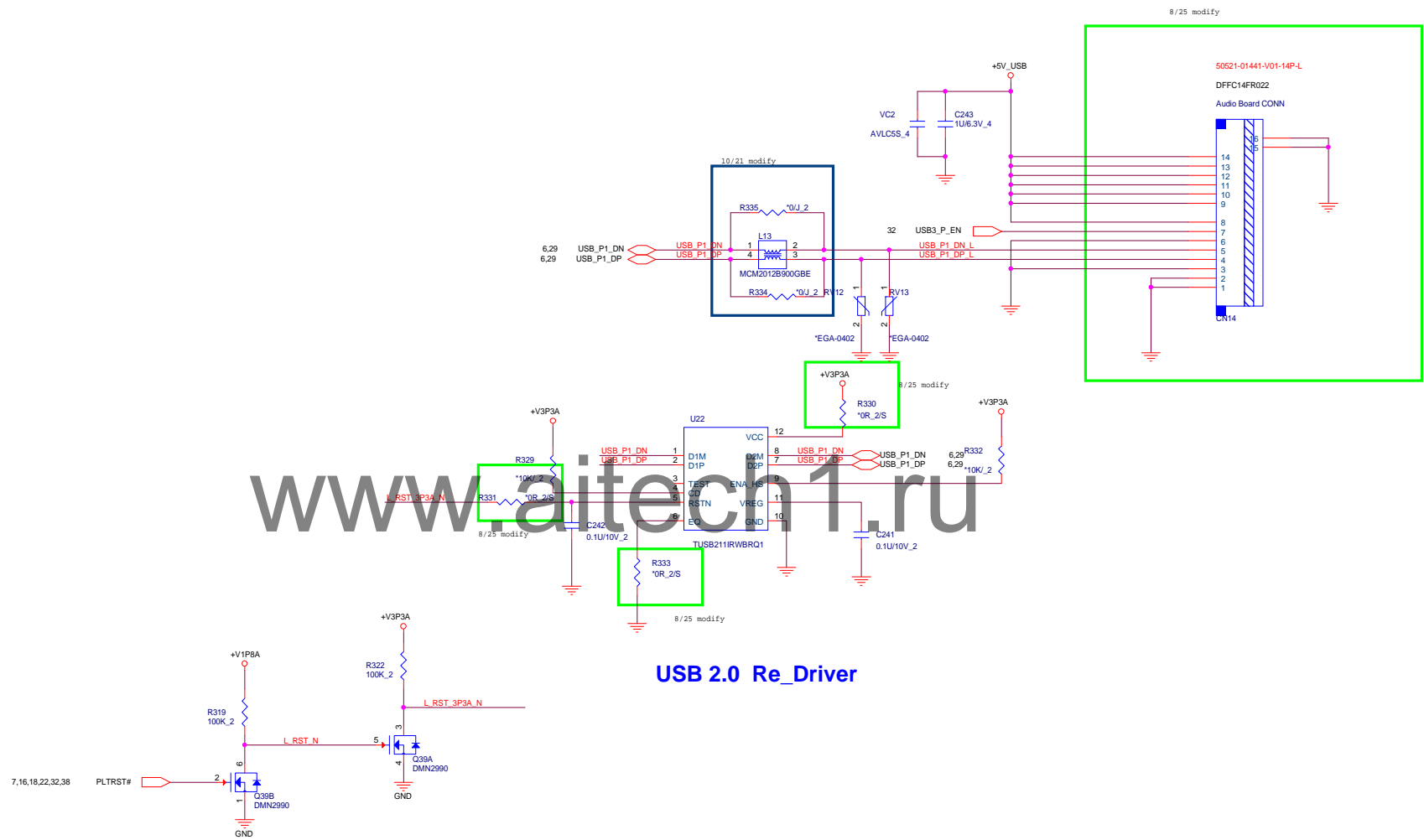
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Input	SD	RD	CP	D	Output	Q
L	H	X	X	X	H	L
H	L	X	X	X	L	H
L	L	X	X	X	H	H

[1] H = HIGH voltage level;
L = LOW voltage level;
X = don't care.

Input	SD	RD	CP	D	Output	Q _{n+1}
H	H	H	↑	L	L	H
H	H	H	↑	H	H	L

[1] H = HIGH voltage level;
L = LOW voltage level;
↑ = LOW-to-HIGH CP transition;
Q_{n+1} = state after the next LOW-to-HIGH CP transition.

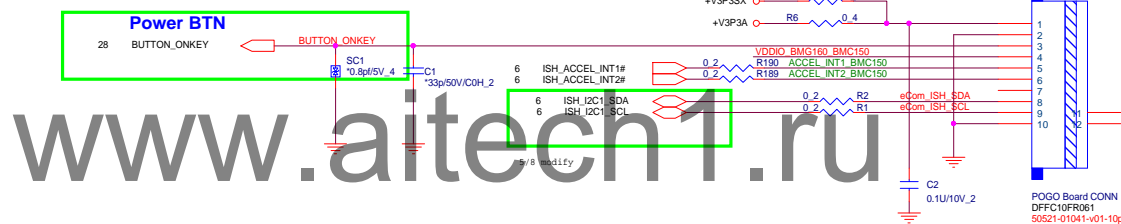
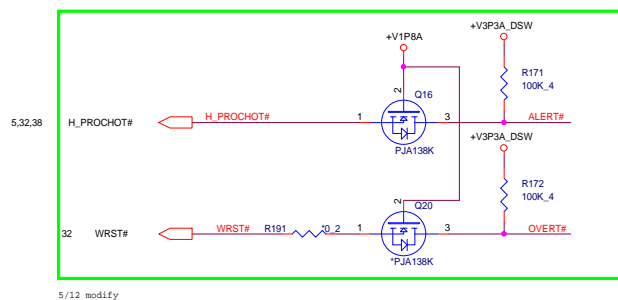
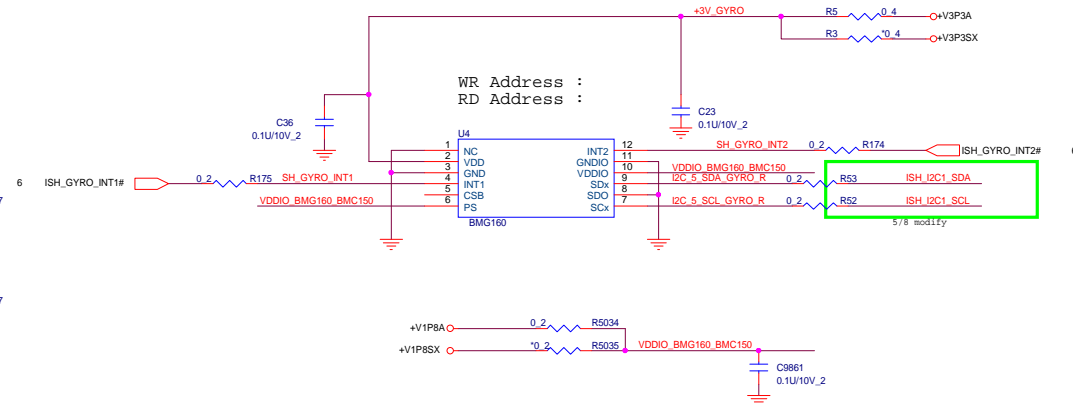
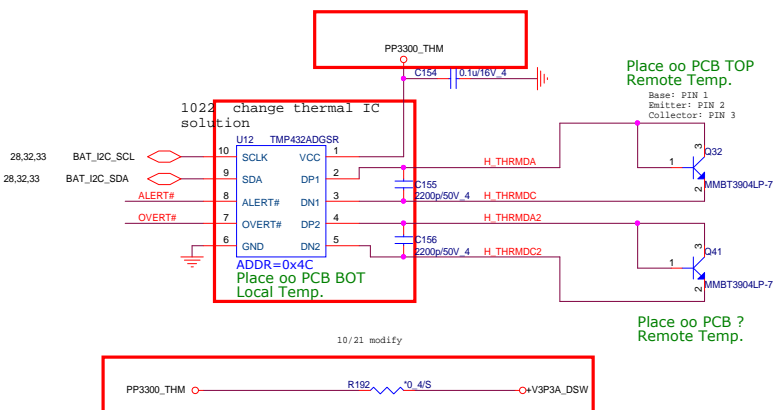




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Gyroscope (BMG160)



E-compass/Magnetometer/Accelerometer (BMC150)

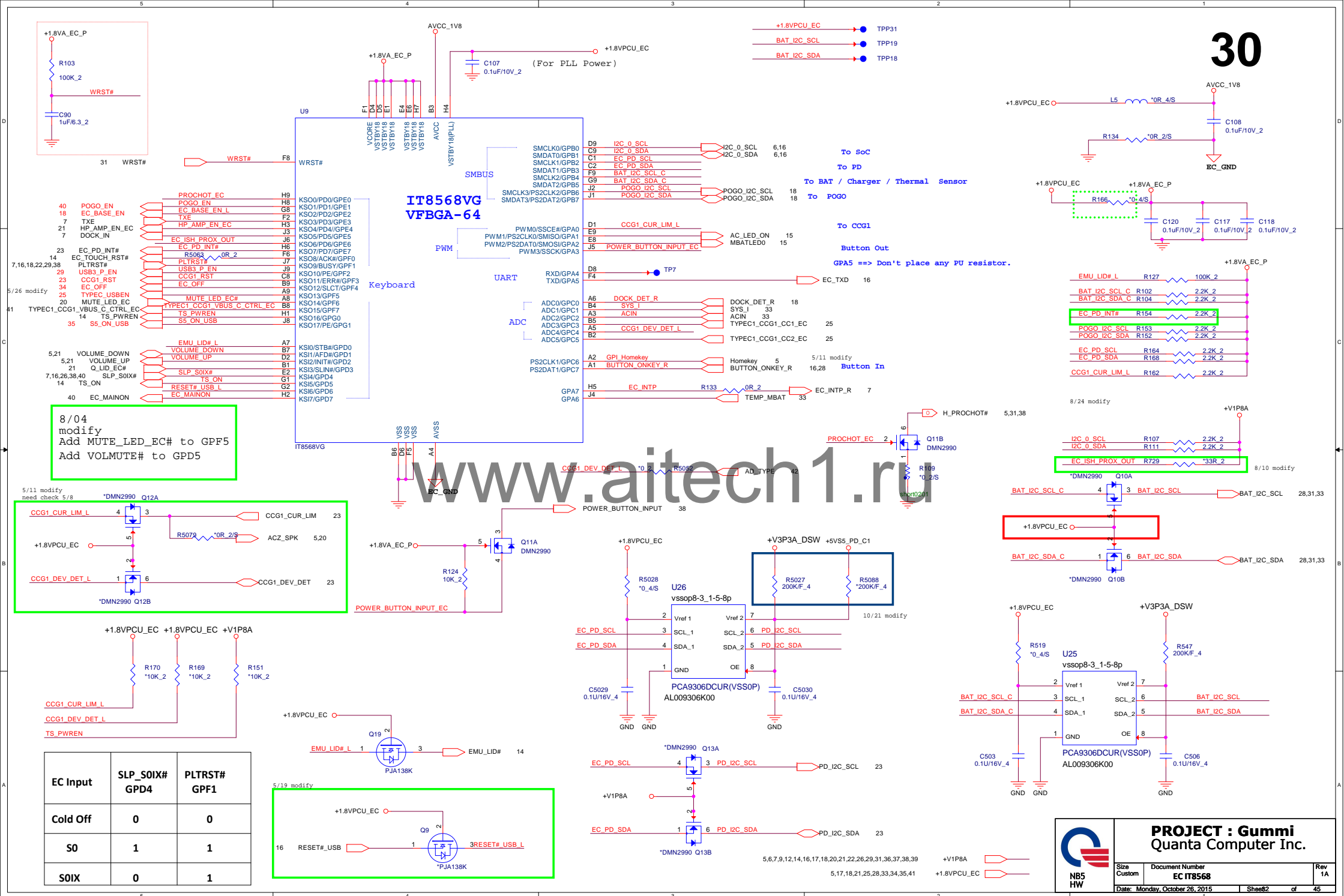
Keepout area is around 10mm

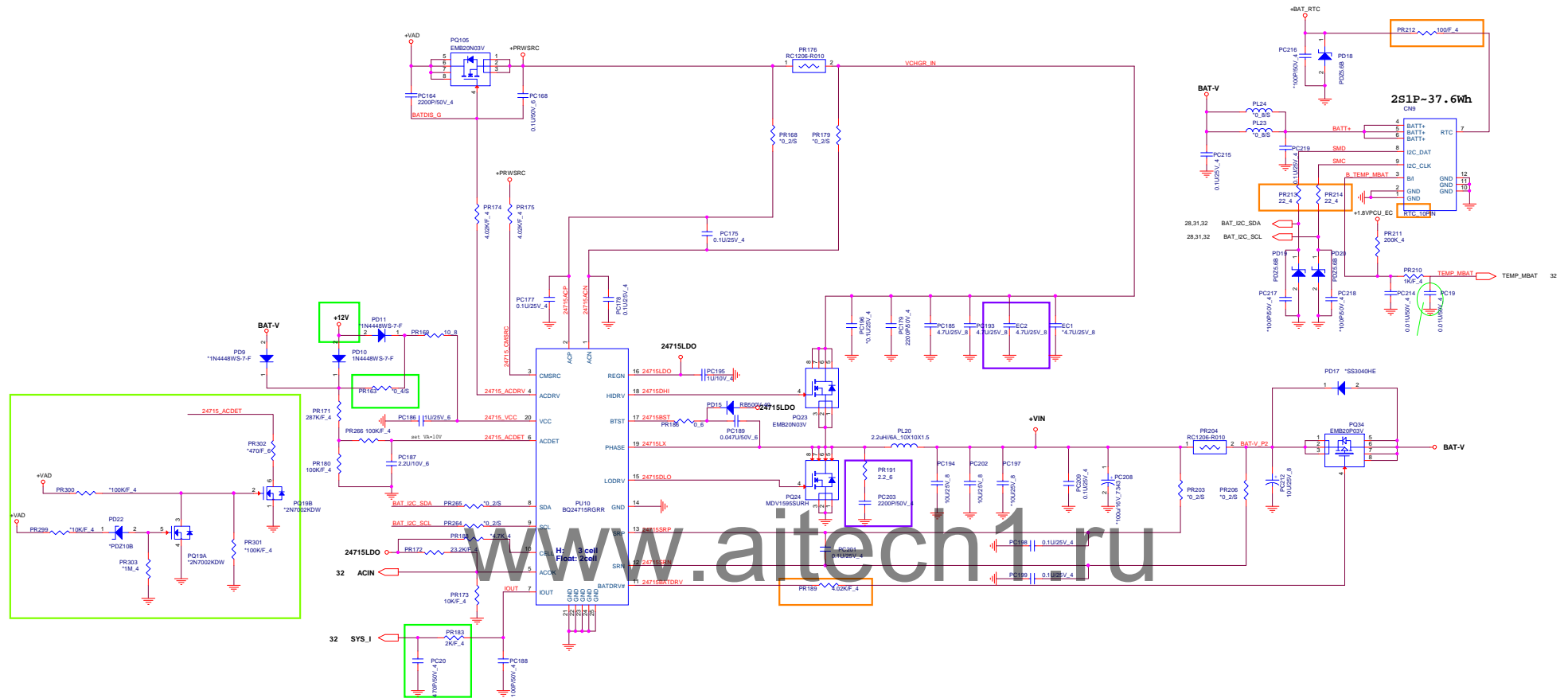
WR Address : 0x3C

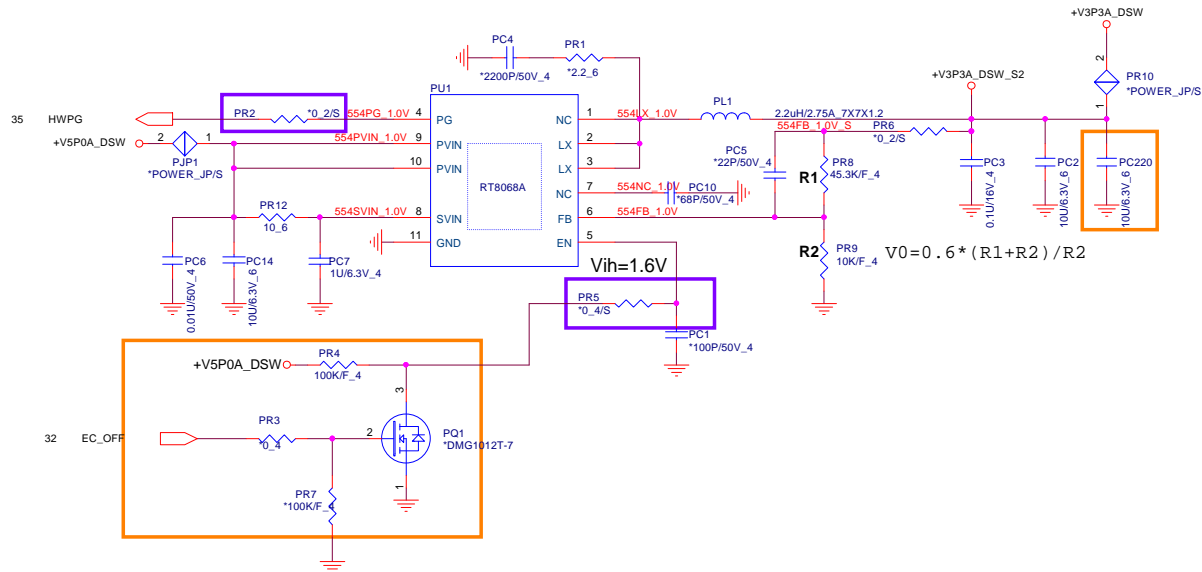
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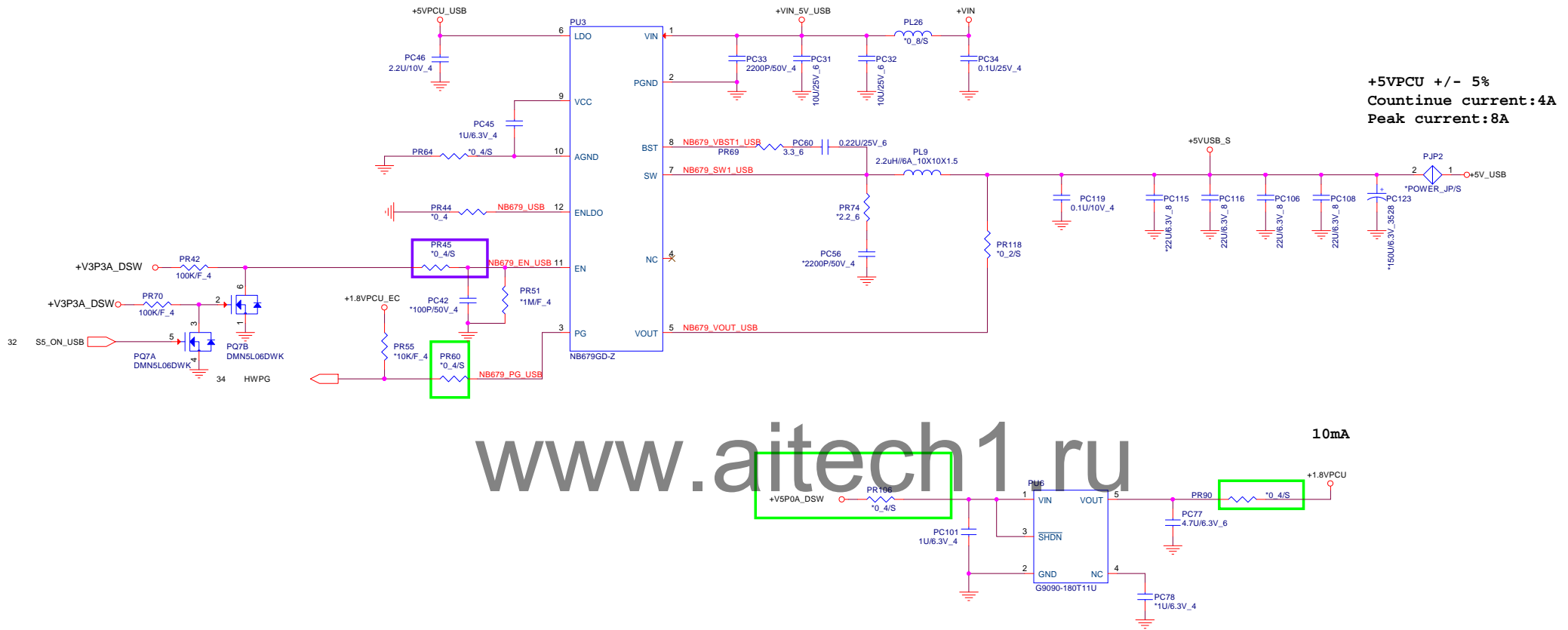
WR Address : 0x3C
RD Address : 0x3D

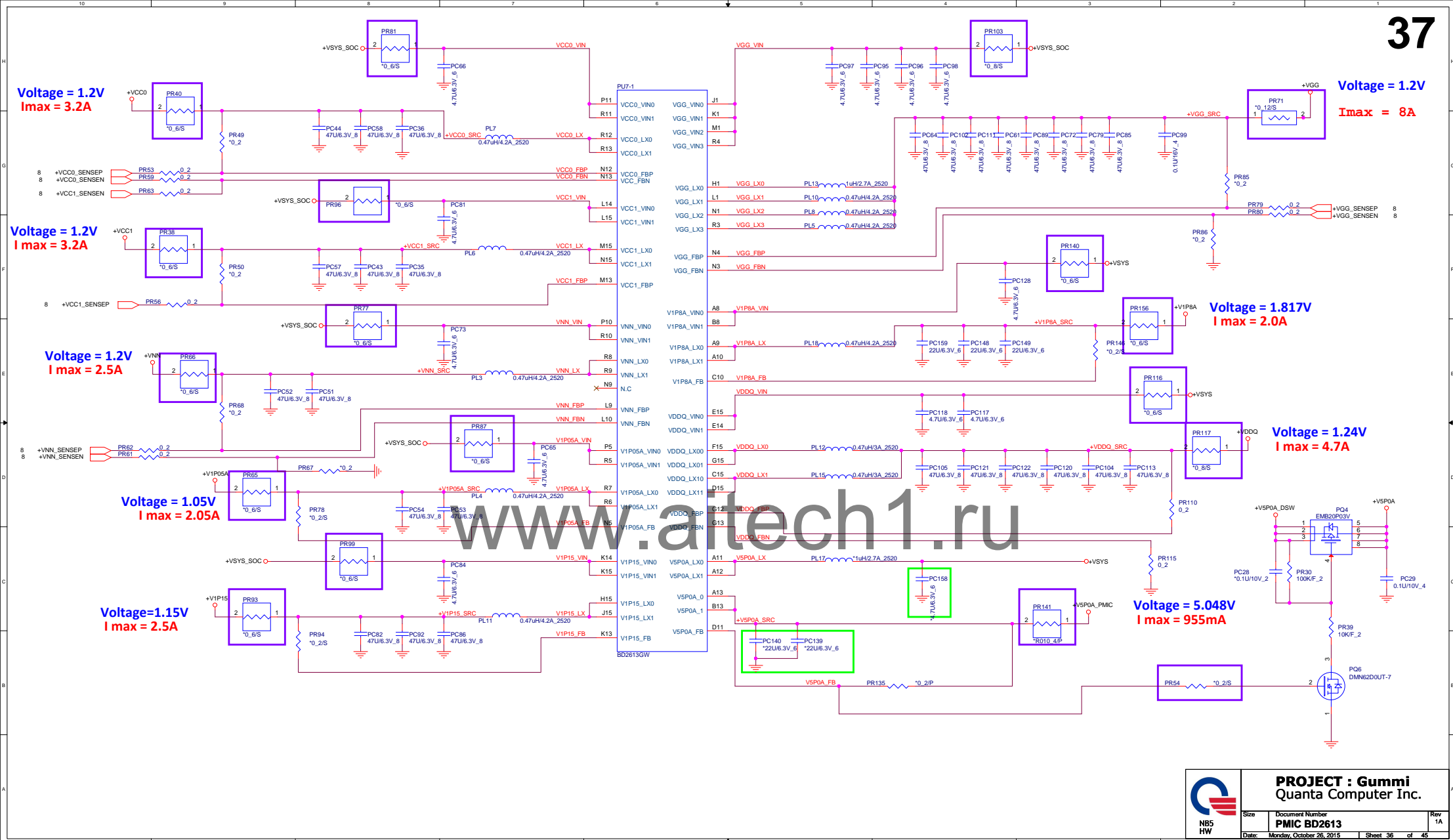
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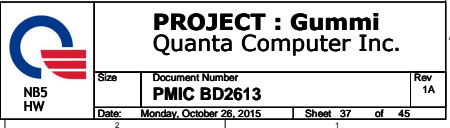




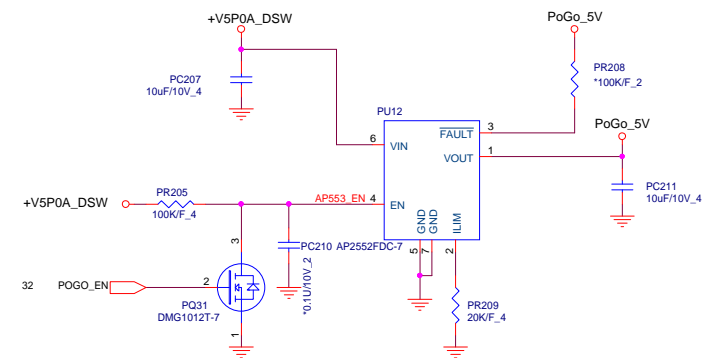




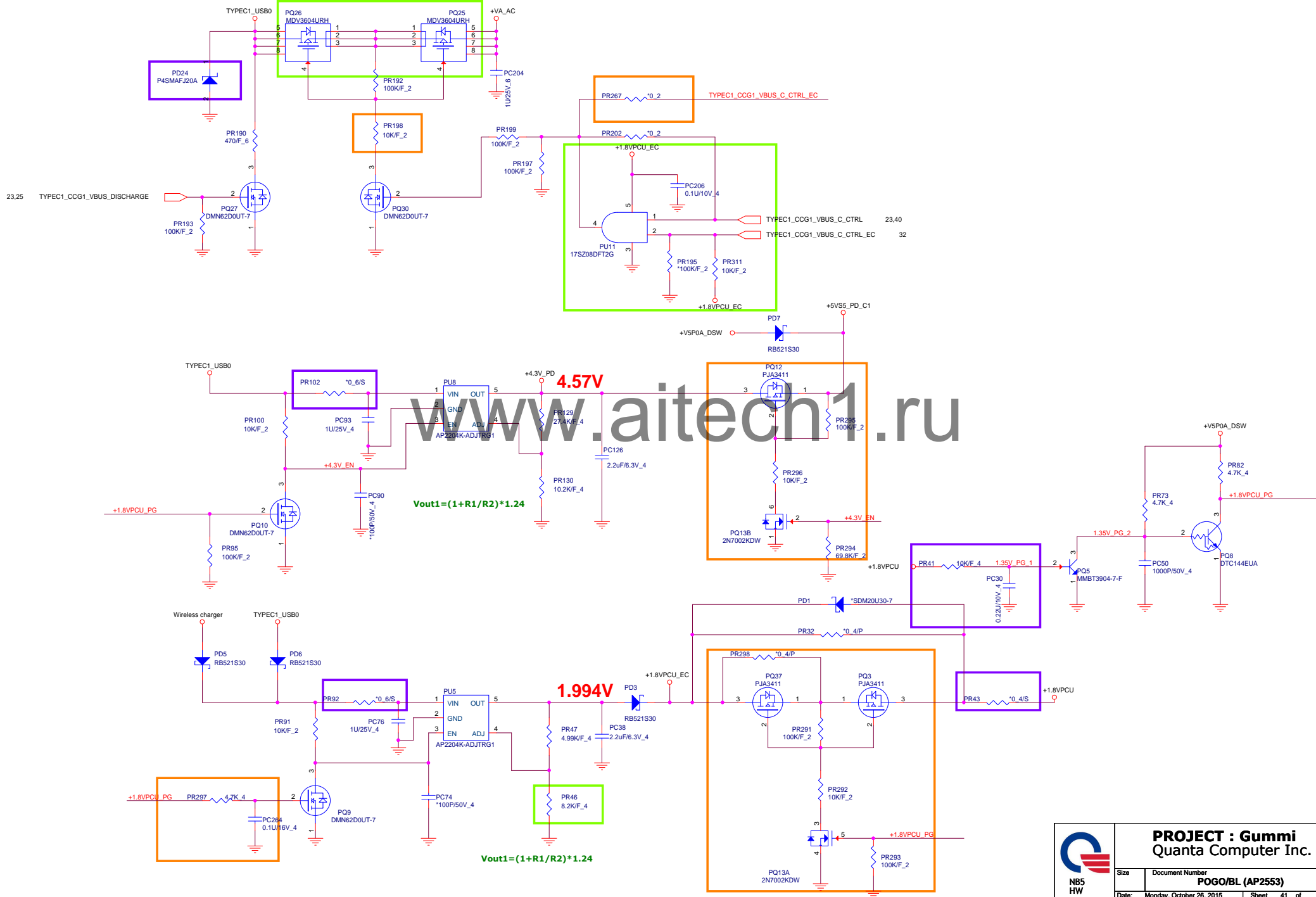








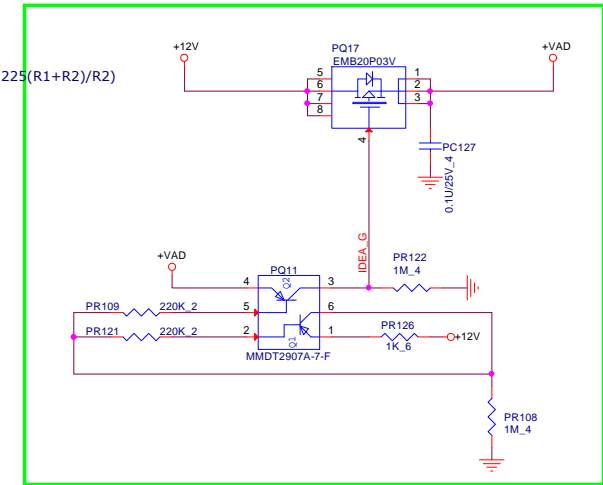
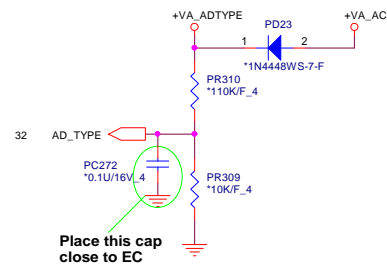
For TypeC PORT1



Type C => 15V~45W
Type C => 12V
Wireless charger => 20V/1A

+12V +/- 5%
Continue current: 2A
OCP minimum 3A

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Custom	Boost	1A
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